Amendments to the Claims

Please add claims 20-28

20.   (new) A memory device, comprising:

at least one block of memory cells;

a metadata block associated with each of the blocks of memory cell;

a counter located on the memory device associated with each metadata block, wherein the counter is operable to track usage of the block of memory cells associated with the metadata block.

21.   (new) The memory device of claim 20, wherein the memory device further comprises a polymer memory device.

22.   (new) The memory of claim 20, wherein the memory device further comprises a ferroelectric memory device.

23.   (new) The memory device of claim 20, wherein the memory device is used as a disk replacement.

24.   (new) The memory device of claim 20, wherein the memory device is used as a non-volatile cache memory.

25.   (new) The memory device of claim 20, wherein the metadata block further comprises flags set and reset during cache operations.

26.   (new) A method of tracking usage of a memory device, the method comprising:

participating an a memory access cycle;

incrementing a counter producing an incremented counter, wherein the counter is associated with a metadata block for a block participating in the memory access cycle;

updating a preexisting error correction code producing an updated error correction code, wherein the error correction code includes new data from the memory access cycle and the incremented counter; and

determining if the counter exceeds a predetermined threshold.

27.   (new) The method of claim 26, wherein incrementing a counter further comprises incrementing a hardware counter implemented in logic on the memory device.

28.   (new) The method of claim 26, the method comprising tracking usage of a polymer memory device.